FIG. 1

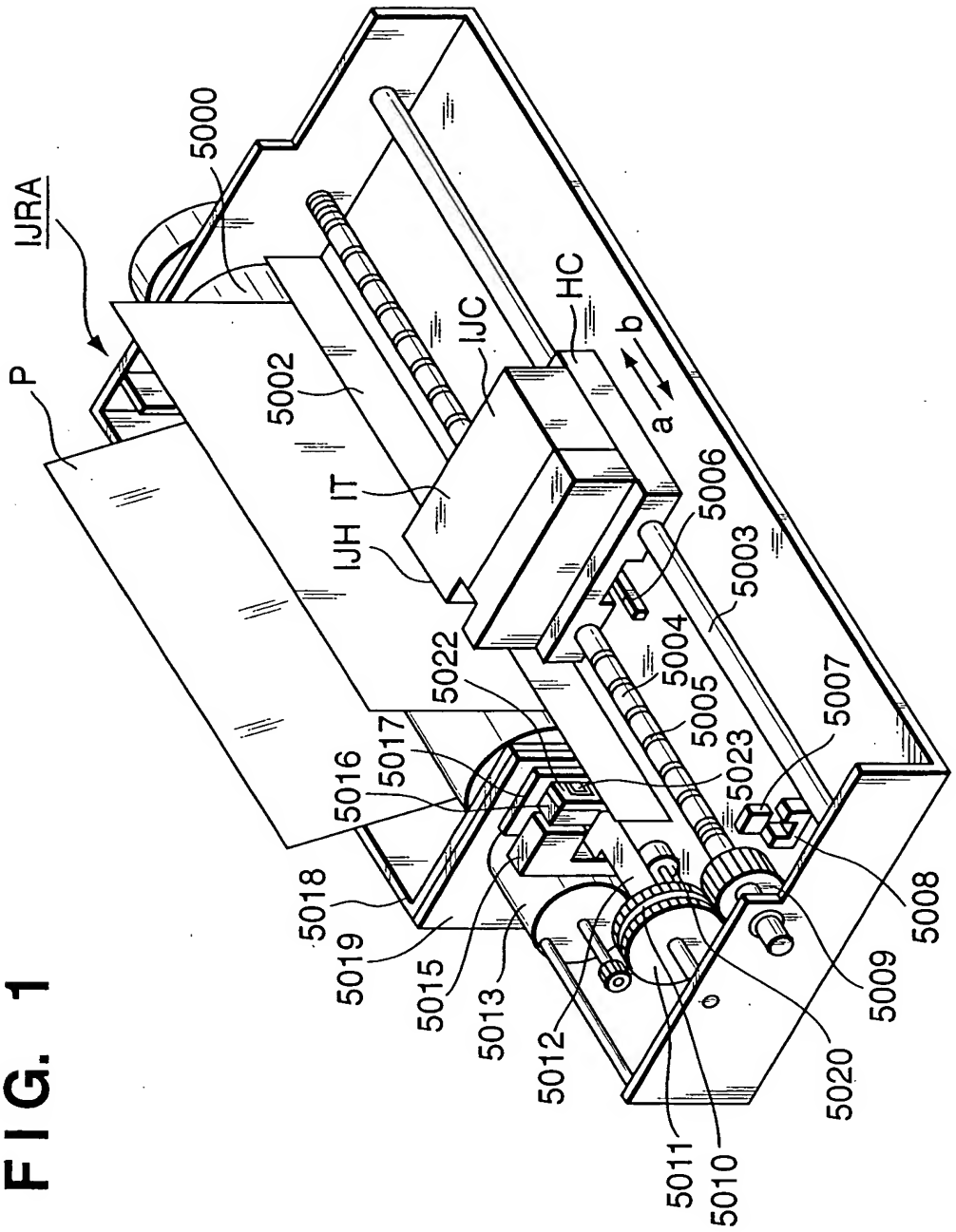
IJRA
5000
P
5002
IJC
HC
b
a
IJH
IT
5006
5003
5004
5022
5005
5007
5017
5016
5023
5018
5019
5015
5013
5012
5008
5009
5020
5011
5010

FIG. 2

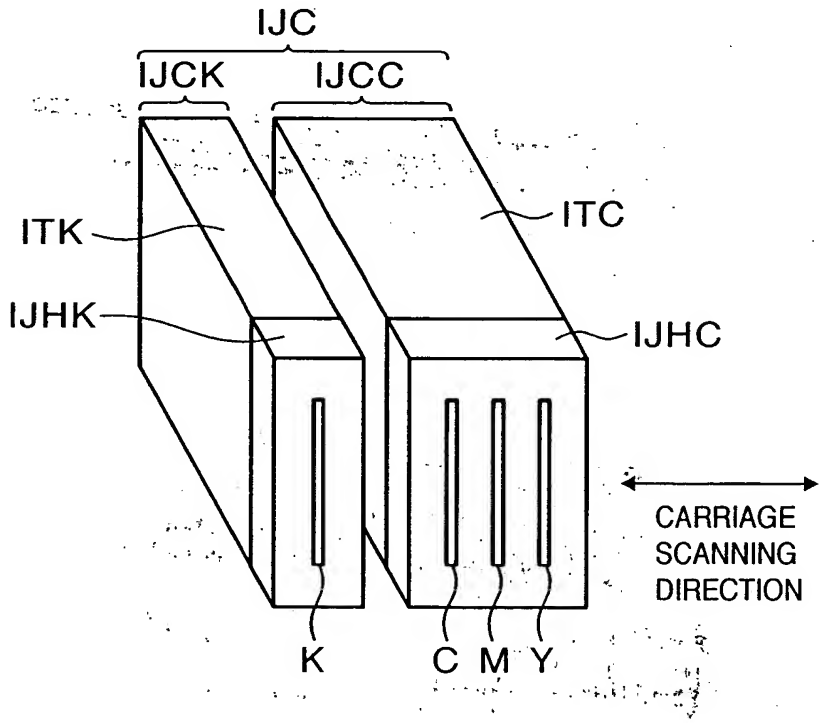
IJC
IJCK
IJCC
ITK
ITC
IJHK
IJHC
CARRIAGE
SCANNING
DIRECTION
K
C
M
Y

FIG. 3

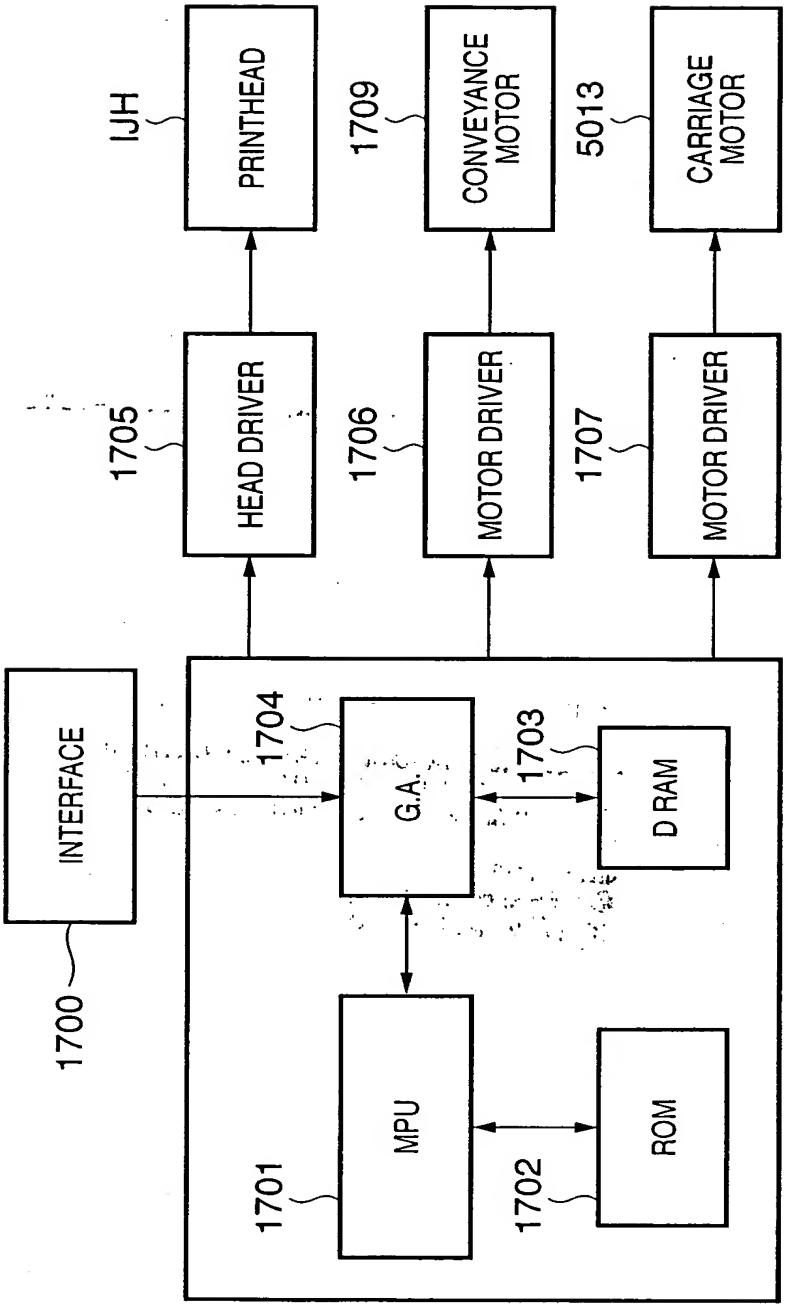
IJH
PRINTHEAD
1709
CONVEYANCE MOTOR
5013
CARRIAGE MOTOR
1705
HEAD DRIVER
1706
MOTOR DRIVER
1707
MOTOR DRIVER
INTERFACE
1704
G.A.
1703
D RAM
1700
MPU
ROM
1701
1702

FIG. 4

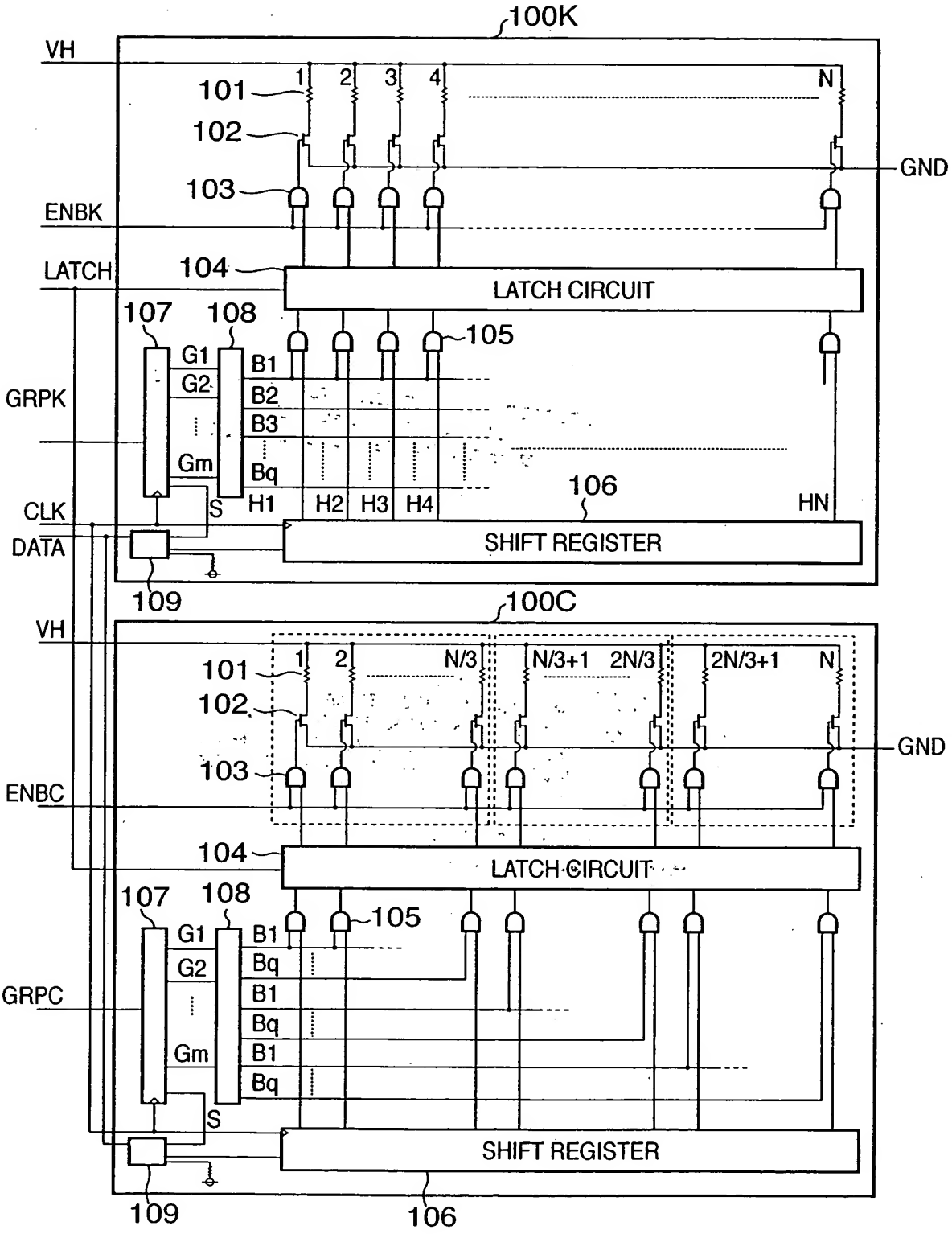
100K
VH
101
102
103
ENBK
LATCH
104
LATCH CIRCUIT
107
108
105
GRPK
G1
G2
Gm
B1
B2
B3
Bq
S
H1
H2
H3
H4
HN
106
CLK
DATA
SHIFT REGISTER
109
GND
1
2
3
4
N
100C
N/3
N/3+1
2N/3
2N/3+1
ENBC
GRPC

FIG. 5

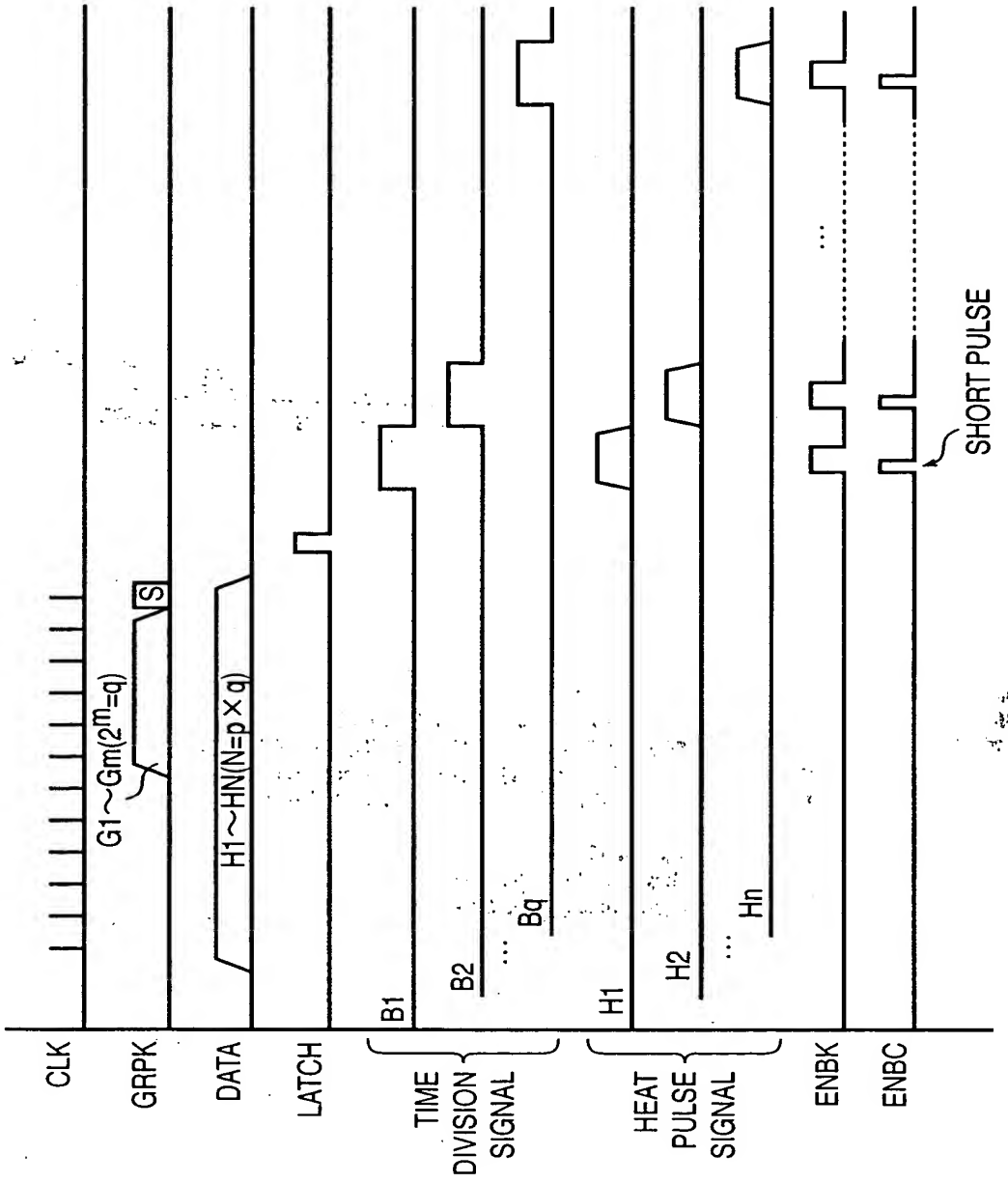
CLK
GRPK
DATA
LATCH
TIME DIVISION SIGNAL
HEAT PULSE SIGNAL
ENBK
ENBC
G1~Gm(2^m=q)
S
H1~HN(N=p×q)
B1
B2
Bq
H1
H2
Hn
SHORT PULSE

FIG. 6

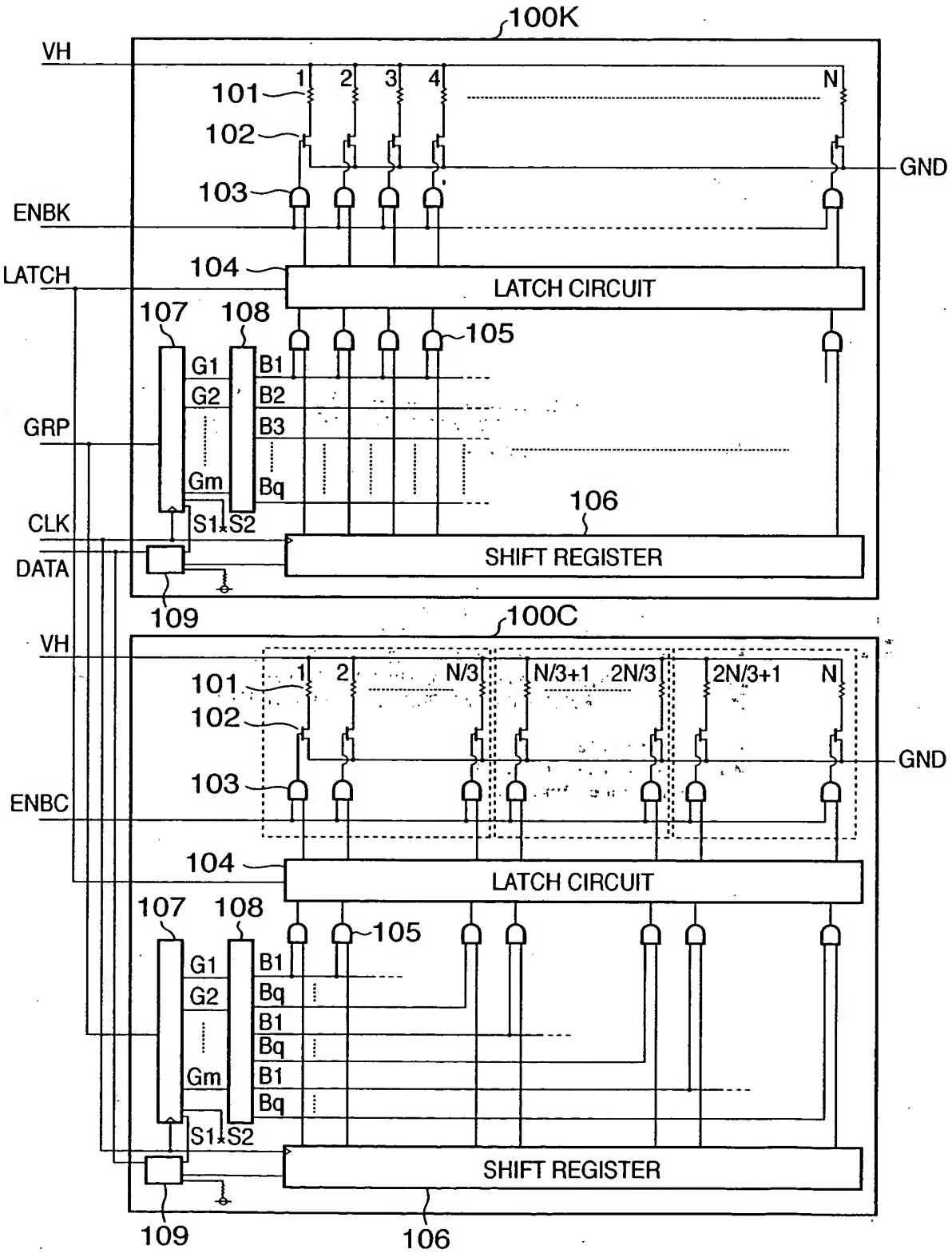
100K
VH
101
1
2
3
4
N
102
GND
103
ENBK
LATCH
104
LATCH CIRCUIT
107
108
105
G1
B1
G2
B2
B3
GRP
Gm
Bq
106
CLK
S1
S2
DATA
SHIFT REGISTER
109
100C
VH
101
1
2
N/3
N/3+1
2N/3
2N/3+1
N
102
GND
103
ENBC
104
LATCH CIRCUIT
107
108
105
G1
B1
G2
Bq
B1
Bq
Gm
B1
Bq
S1
S2
SHIFT REGISTER
109
106

FIG. 7

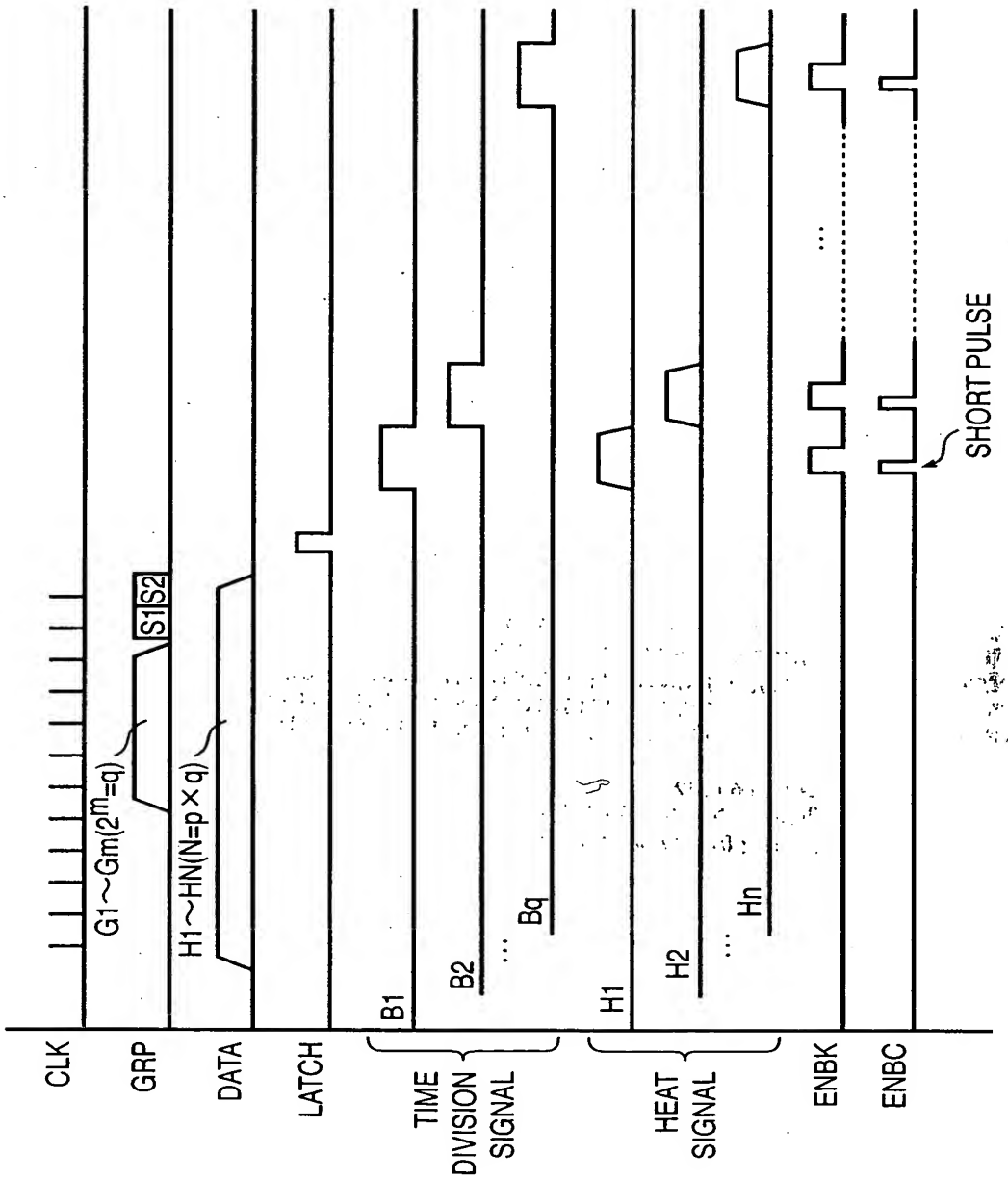
CLK
GRP
DATA
LATCH
TIME DIVISION SIGNAL
HEAT SIGNAL
ENBK
ENBC
G1~Gm(2^m=q)
S1S2
H1~HN(N=p×q)
B1
B2
Bq
H1
H2
Hn
SHORT PULSE

FIG. 8

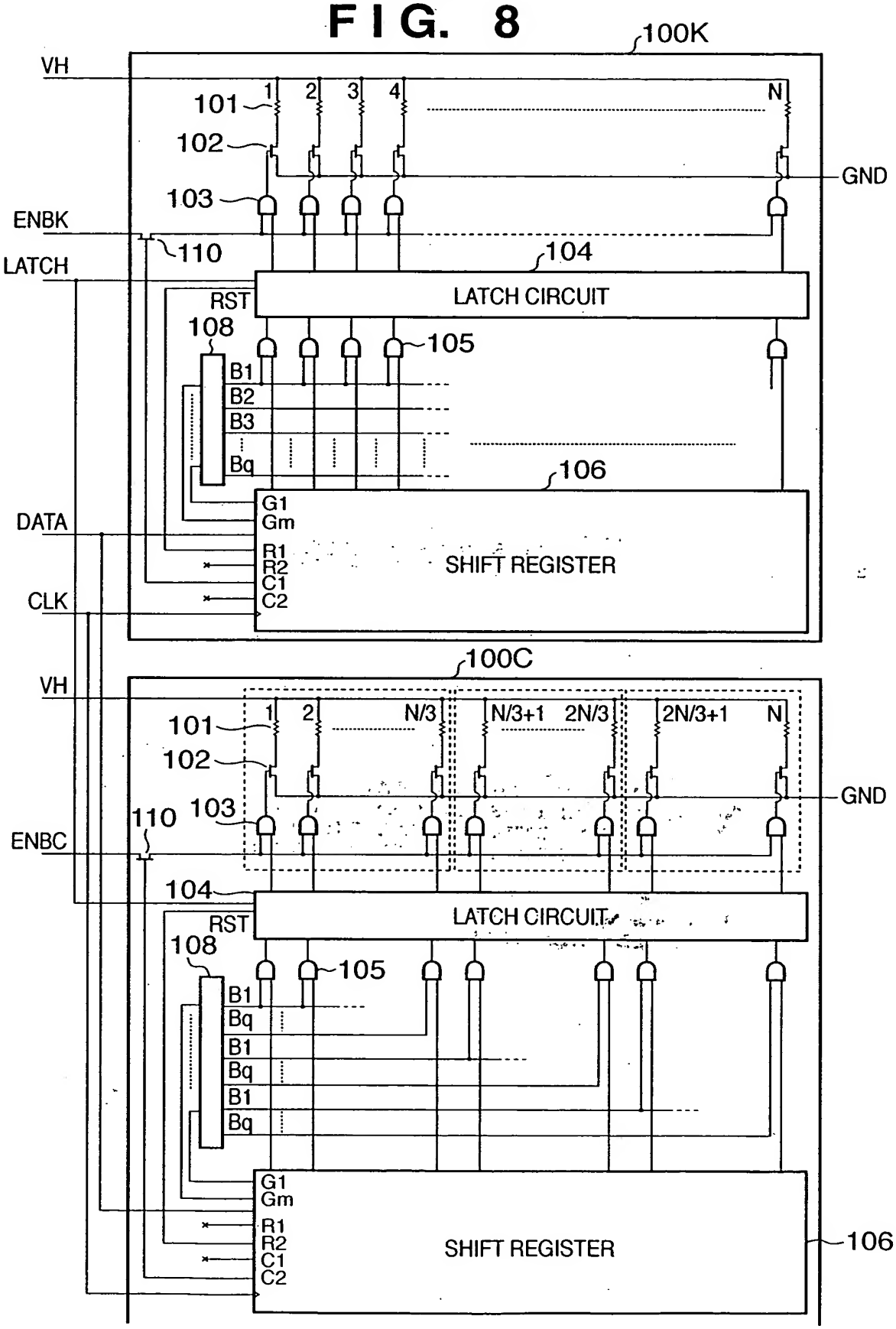
100K
VH
101
1
2
3
4
N
102
GND
103
ENBK
110
LATCH
104
LATCH CIRCUIT
RST
108
105
B1
B2
B3
Bq
106
G1
Gm
DATA
R1
R2
SHIFT REGISTER
C1
C2
CLK
100C
VH
101
1
2
N/3
N/3+1
2N/3
2N/3+1
N
102
GND
110
103
ENBC
104
LATCH CIRCUIT
RST
108
105
B1
Bq
B1
Bq
B1
Bq
G1
Gm
R1
R2
SHIFT REGISTER
106
C1
C2

FIG. 9

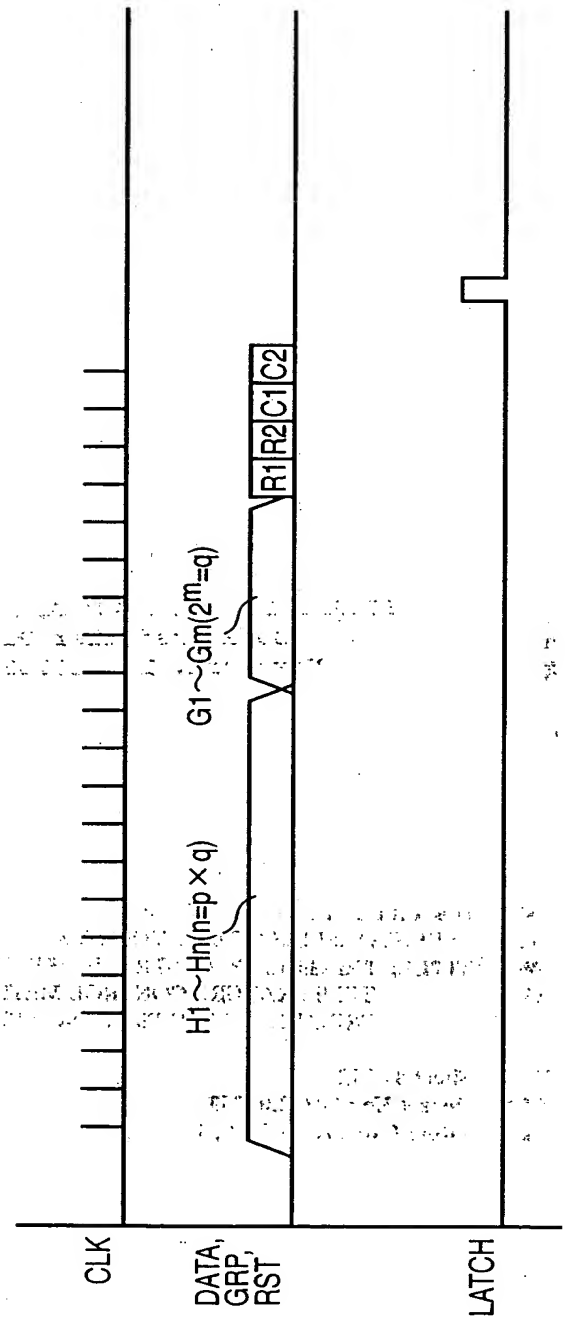

FIG. 10

TRUTH TABLE OF LATCH CIRCUIT

INPUT		OUTPUT
DATA	RESET	
L	L	L
H	L	H
L	H	H
H	H	H

FIG. 11

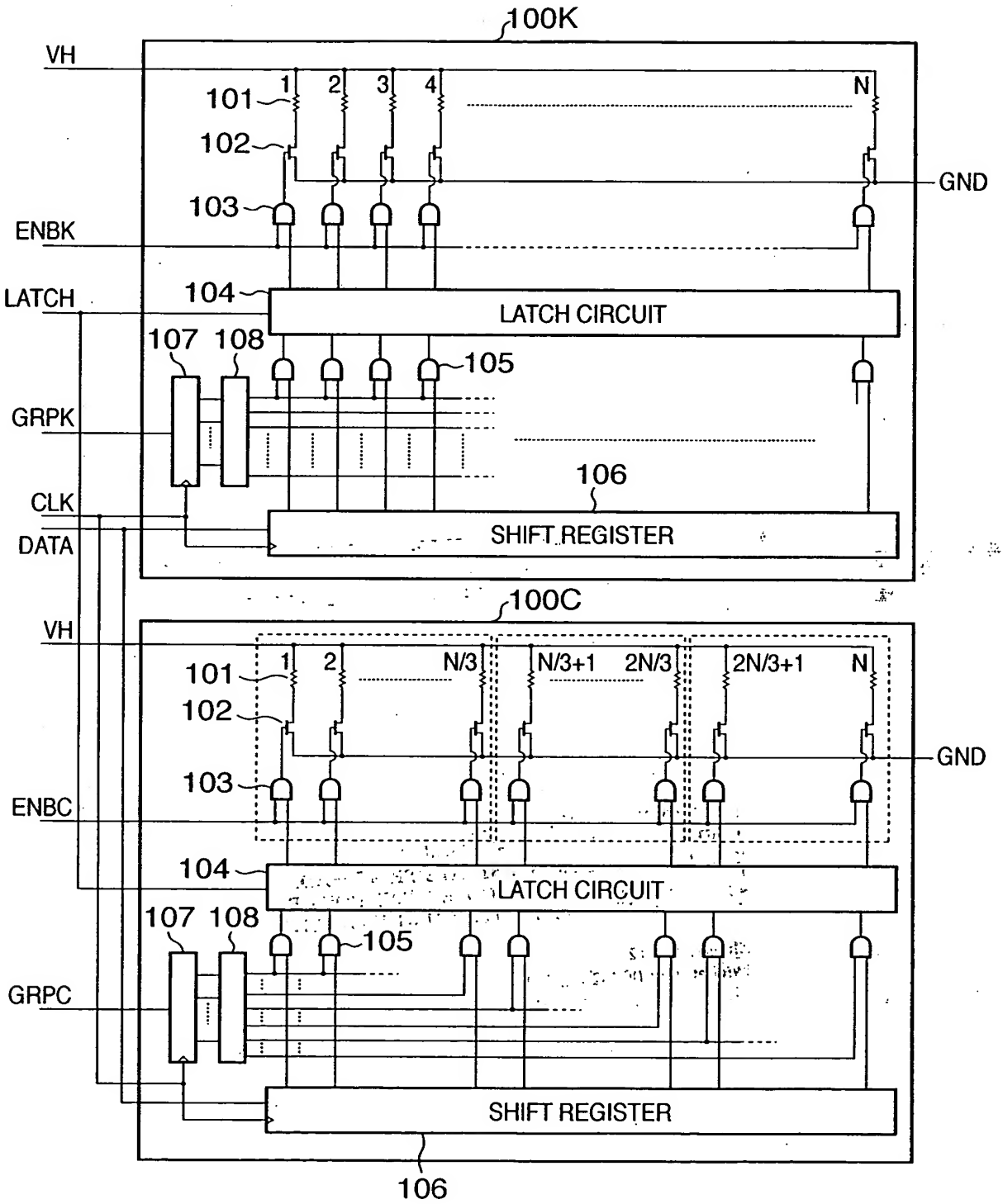
100K
VH
101
1
2
3
4
N
102
GND
103
ENBK
LATCH
104
LATCH CIRCUIT
107
108
105
GRPK
106
CLK
SHIFT REGISTER
DATA
100C
VH
101
1
2
N/3
N/3+1
2N/3
2N/3+1
N
102
GND
103
ENBC
104
LATCH CIRCUIT
107
108
105
GRPC
SHIFT REGISTER
106

FIG. 12